

# APPLICATION

## FOR

# UNITED STATES LETTERS PATENT

APPLICANT NAME: Mukta G. Farooq  
Raymond A. Jackson  
David C. Linnell

TITLE: REWORK METHODS FOR LEAD FREE  
BGA/CGA

DOCKET NO.: FIS920010111US1

INTERNATIONAL BUSINESS MACHINES CORPORATION

ARMONK, NEW YORK

### CERTIFICATE OF MAILING UNDER 37 CFR 1.10

I hereby certify that, on the date shown below, this correspondence is being deposited with the United States Postal Service in an envelope addressed to the Assistant Commissioner for Patents, Washington, D.C., 20231 as "Express Mail Post Office to Addressee"

Mailing Label No. EF383029895 US

on 7/24/01

Karen Cing-Mars

Name of person mailing paper

Signature

Date

ibmf100337000

# REWORK METHODS FOR LEAD BGA/CGA

## Background Of The Invention

### 1. Field of the Invention

This invention relates generally to electronic component assemblies wherein the electronic components are joined together by solder interconnections and, more particularly, to the rework of such assemblies by removing one component from another joined component.

### 2. Description of Related Art

Forming an electronic package assembly whereby an electrical component such as an integrated circuit chip is electrically and mechanically connected to a substrate such as a card, or board, another chip or another electronic part is well-known in the art. This technology is generally termed surface mount technology (SMT) and has gained acceptance as the preferred means of making electronic package assemblies. In one particular application, to which this application is directed to for convenience, multilayer ceramic components, as exemplified by integrated circuit chips, are joined to printed circuit cards or boards which boards are to be reworked by removing defective or unwanted chips from the board.

Multilayer ceramic electronic components are typically joined to printed circuit boards by soldering pads on a surface of one of the electronic components to corresponding soldering pads on the surface of the other component. Control Collapse Chip Connection is an interconnect technology developed by IBM as an alternative to wire bonding. This technology is generally known as C4 technology or flip chip packaging. Broadly stated, an integrated circuit chip is mounted above a board and pads on the chip are electrically and mechanically connected to corresponding pads on the board by a plurality of electrical connections such as solder bumps. The integrated circuit chips may be assembled in a solder array such as 10 x 10 array.

In the C4 interconnect technology, a relatively small solder bump is attached to the pads on one of the components being joined, typically to the chip. The electrical and mechanical interconnects are then formed by positioning the corresponding pads on the board to be joined adjacent the solder bumps on the chip and reflowing the bumps at an elevated temperature. The C4 joining process is self-aligning in that the wetting action of the solder will align the chip bump pattern to the corresponding pads on the board.

A myriad of solder structures have been proposed for the surface mounting of one electronic structure to another. Typical surface mount processes form the solder structures by screening solder paste on conductive, generally metallic pads exposed on the surface of the first electronic structure or substrate. A stencil printing operation is used to align the contact mask to the pads. The solder paste is reflowed in an inert atmosphere and homogenizes the pad and brings the solder into a spherical shape. The solder spheres on the substrate are then aligned to corresponding pads on the electronic structure or board to be connected thereto. After alignment, the substrate and board go through a reflow operation to melt the solder and create a solder bond between the corresponding pads on the substrate and other electronic component. The interconnection is typically in a form of a double truncated sphere and is termed a ball grid array (BGA). When the solder structure is in the form of a column, it is termed a column grid array (CGA).

It has now been proposed to use lead free solders in BGA/CGA interconnect schemes and these solders generally necessitate the use of higher temperatures during reflow attachment. Even higher than the attach temperatures are the temperatures required to do rework for removing defective modules and components, typically if the existing rework methods and tools are to be used. These excessive temperatures (typically above  $\sim 250^{\circ}\text{C}$ ), can cause irreparable damage to the organic board substrate. Current methods require the use of such high temperatures to ensure that the solder joints reach the liquidus or eutectic temperature of the alloy ( $\sim 221^{\circ}\text{C}$  for eutectic Sn/Ag and  $\sim 217^{\circ}\text{C}$  for eutectic Sn/Ag/Cu). Rework of lead free alloys

containing assemblies has become a critical issue in the qualification of this technology because of the high rework temperatures required.

Bearing in mind the problems and deficiencies of the prior art, it is therefore an object of the present invention to provide a method for separating electronic components joined by solder connections and, in particular, lead-free solder connections in a BGA or CGA interconnect assembly such as a chip joined to a printed circuit board.

It is another object of the present invention to provide an apparatus for separating electronic components joined by solder connections and, in particular, joined together by lead-free solders in a BGA or CGA interconnect assemblies such as a chip joined to a printed circuit board.

It is yet another object of the present invention to provide an electronic component assembly reworked using the method and apparatus of the invention.

Still other objects and advantages of the invention will in part be obvious and will in part be apparent from the specification.

### **Summary of the Invention**

The above and other objects and advantages, which will be apparent to one of skill in the art, are achieved in the present invention which is directed to, in a first aspect, a method for separating electronic components joined by solder connections comprising the steps of:

- supplying an electronic component assembly having at least two components joined by a plurality of solder interconnections typically arranged in a row-by-row configuration and having a first thickness;
- providing a cutting element having a thickness less than the first thickness of the solder interconnections;
- heating the cutting element to a temperature sufficient to melt the solder at the point of contact when the cutting element is in contact with and forced against the solder interconnections;

positioning the heated cutting element adjacent a row of the solder interconnections;  
applying a force to advance the heated cutting element through the row of solder  
interconnections whereby the heated cutting element engages and cuts through the  
solder interconnections and severs the row of solder interconnections;  
5 continuing the above steps to cut and sever all the solder interconnections; and  
separating the two components.

In a further aspect of the invention, the cutting element is a wire or a blade and is  
preferably moved sideways to the direction of travel of the cutting device either in one  
direction or in a reciprocating motion to provide a sawing effect as the cutting element is  
10 moved forward through the rows of solder interconnections.

In another aspect of the invention, the cutting element in the cutting device is a  
water knife which forms a high pressure water stream directed as a jet at the rows of  
solder interconnections to provide a cutting action to cut and sever the rows of solder  
interconnections.

15 In a further aspect of the invention, an apparatus is provided for separating  
electronic components joined by solder interconnections comprising:

securing means to hold an electronic assembly having at least two components joined  
by a plurality of solder interconnections arranged typically in a row-by-row  
configuration and having a first thickness;

20 a cutting element having a thickness less than the thickness of the solder  
interconnections;

a heater to heat the cutting element to a temperature sufficient to melt the solder at the  
point of contact when the cutting element is contacted with and forced forward  
against the solder interconnections;

25 positioning means to position the heated cutting element adjacent one of the rows of  
solder interconnections;

advancing means to force the heated cutting element against the row of solder  
interconnections and through the row of solder interconnections whereby the  
heated cutting element cuts and severs the row of solder interconnections; and

separating means to separate the two components when all the solder interconnections have been cut and severed by the heated cutting element.

In an additional aspect of the invention chips and other electronic components secured to a printed circuit board or other electronic component may be separated and/or  
5 reworked by removing one of the joined components e.g., a chip, and securing a new component, e.g., a chip to the other component, e.g., printed circuit board, using the method and apparatus of the invention.

### **Brief Description of the Drawings**

The features of the invention believed to be novel and the elements  
10 characteristic of the invention are set forth with particularity in the appended claims. The figures are for illustration purposes only and are not drawn to scale. The invention itself, however, both as to organization and method of operation, may best be understood by reference to the detailed description which follows taken in conjunction with the accompanying drawings in which:

15 Fig. 1 is an enlarged perspective schematic view of an electronic component assembly comprising a substrate having chips attached thereto with rows of C4 solder ball interconnections.

Fig. 2A is a schematic cross-sectional view of Fig. 1 taken along line 2-2 and an apparatus of the invention used to cut the C4 solder ball interconnections using the  
20 method of the invention.

Figs. 2B-2D show a sequence of operations in which the rows of solder balls of Fig. 2A are cut and severed so that the chip may be removed from the substrate.

Figs. 3A-3D show different schematic devices of the invention for cutting and severing the C4 solder ball interconnections.

25 Figs. 4A and 4B show other devices of the invention for cutting and severing the C4 solder ball interconnections.

### Description of the Preferred Embodiment(s)

In describing the preferred embodiment of the present invention, reference will be made herein to Figs. 1-4B of the drawings in which like numerals refer to like features of the invention. Features of the invention are not necessarily shown to scale  
5 in the drawings.

Referring to Fig. 1, an electronic component assembly of the prior art is shown generally as 10. The assembly comprises a substrate 11 having another electronic component 12 attached thereto by a plurality of solder balls 13. The solder balls 13 are typically in a row-by-row array such as shown in the figure which is a 3  
10 x 3 array which would contain nine (9) solder ball interconnections. Typically the arrays could be 25 x 25 or higher. The solder balls are numbered by rows as 13a, 13b and 13c.

The substrate 11 and other electronic component 12 may be any electronic components as is well known in the art. For convenience the following description  
15 will be directed to substrate 11 being an organic printed circuit board and the electronic component 12 attached thereto being a chip. Also, the solder interconnections 13 will be referred to as solder balls (e.g., ball grid array (BGA)) although it will also be appreciated by those skilled in the art that other solder interconnections such as columns (e.g., column grid array (CGA)) may also be used  
20 to attach the components.

Referring now to Fig. 2A, this figure shows a cross-sectional view of the electronic component assembly of Fig. 1 taken along line 2-2. The printed circuit board substrate 11 is shown having a chip 12 electrically connected thereto by solder balls 13 in a 3 x 3 array as shown in Fig. 1. The solder balls are connected by pads  
25 14 on the chip 12 and pads 18 on the printed circuit board 11. The solder balls are in the form of double truncated spheres.

The object of the invention is to sever (cut) the solder balls 13 without damaging the chip and/or board so that the chip 12 may be separated from the printed circuit board 11 for rework or other purposes.

A cutting device is shown generally as 15 and comprises a vertical structural member 16 having a wire 17 attached thereto near the bottom of the structural member. The cutting device 15 is positioned over the printed circuit board 11 and is to be moved forward in the direction of the arrow toward the first solder ball row 13a. A vise or other securing device 28 is used to hold the chip (and/or the board) secure during the cutting operation.

It is an important feature of the invention that the wire 17 be heated and maintained at a suitable temperature during the method of the invention to cut the row of solder balls 13a (and next rows of solder balls 13b and 13c) and sever each of the solder balls so that the printed circuit board 11 can be removed from the chip 12.

Heating of the wire 17 may be done using any suitable means and will preferably comprise using a heating element in structural member 16 or externally and then fed to the wire. The temperature of the wire 17 is sufficient to melt the row of solder balls 13 at the point of contact between the wire and the solder preferably by the temperature of the wire and the frictional force applied to the solder balls by the forward movement of the cutting device 15. It should be appreciated that some of the solder not contacted by the wire will melt but this is preferably minimal. Also, the heat transferred to the circuit board or chips is also minimal so that the temperature of these components does not rise appreciably to cause damage to the component.

Alternatively, a blade could be employed instead of a wire, whereby the blade acts as a plow and removes a portion of the molten solder from the interconnection joint. This would prevent reattachment of the molten solder after the wire passes through the joint. The blade is preferably angled as shown in Fig. 3C such that the removed solder is pushed (plowed) out the side of the component where preferably a vacuum is used to remove the solder.

Preferably, as shown in Figs. 4A and 4B, the wire or blade is moved transverse to the forward motion of the device 15 to effect both a solder ball cutting and sawing effect.



The cutting device 15 shown in Fig. 2A uses only a heated wire 17 and forward movement of the wire and does not show the use of a transverse moving wire to also saw the solder ball and enhance the cutting of the rows of solder balls 13.

After the wire 17 is heated to a sufficient temperature it is moved forward  
5 toward the row of solder balls 13a in the direction of the arrow and, as shown in Fig. 2B, has passed the first solder ball array 13a severing each of the three (3) solder balls in the row of solder balls 13a into an upper portion 19 and a lower portion 20. An opening 25 is thus formed so that each solder ball in row 13a is now separated from the components 12 and 11 which were joined together by the row of solder  
10 balls.

The forward motion of cutting device 15 is continued in the direction of the arrow and, as shown in Fig. 2C, has passed the solder ball row 13c and has likewise severed or cut all three rows of solder balls 13a, 13b and 13c so that all the solder balls in the three rows are now separated into an upper portion 19 and a lower portion  
15 20.

As shown in Fig. 2D the chip component 12 has been lifted from the printed circuit board 11 so that the chip 12 can now be replaced or the assembly otherwise reworked to form the desired assembly.

It will be appreciated by those skilled in the art that it is an important feature  
20 of the invention to cut the solder balls connecting the two electronic components using a heated wire or other heated implement as discussed hereinbelow and as shown in the other figures.

The wire 17 shown in Figs. 2A-2C used to cut the solder balls may be any suitable wire of any suitable thickness. The thickness of the wire will typically  
25 depend on the height h of the solder ball 13 and preferably for a solder ball height of about 0.02 inch a wire diameter of about 0.005 inch is suitable. The wire may be selected from a wide variety of materials, and not limited to, such as tungsten, molybdenum, high carbon steel and brass.

As noted above, it is a preferred embodiment of the invention that the wire be moved sideways relative to the forward motion of the cutting device to provide a frictional or sawing action to the solder balls as well as a heating and cutting of the solder ball due to the temperature and forward motion of the wire. Such a sawing action may be accomplished by moving the wire 17 in an endless loop as shown in Fig. 4A so that the wire 17 is moving at a direction transverse to the direction of movement of the cutting device 15 and provides a sawing or cutting frictional action on the solder ball surface. The wire revolves around pulleys 27a and 27b in a continuous endless loop in the direction of the arrow to provide a sawing action in addition to the cutting action provided by the elevated temperature and forward motion of the wire 17.

The wire may have an abrasive or other cutting member on the surface of the wire to increase the sawing action of the wire. It is an important feature of the invention however, that the wire 17 be heated to a sufficient temperature to effect cutting of the solder balls and the temperature will be dependent on the cutting action of the wire with a lower wire temperature being typically used for a highly abrasive wire. In general, the temperature of the wire will exceed the liquidus temperature of the solder being cut.

Referring now to Figs. 3A-3D, various embodiments of the cutting device 15 of the invention are shown in schematic.

In Fig. 3A a cutting device 15 is shown which is the same as the cutting device 15 shown and used in Figs. 2A-2D. Thus, two vertical structural members 16 are joined by a horizontal upper member 26 to form a cutting device 15 which may be moved along the surface of the circuit board or other substrate which is connected to another electronic component to be separated therefrom. A wire 17 extends between the vertical structural members 16 and is positioned at a height which will be intermediate the height  $h$  of the solder ball when the cutting device 15 is moved forward across the surface of the printed circuit board.

Another embodiment of the wire containing cutting device 15 of Fig. 3A is shown in Fig. 4A in which the wire may be circulated around an endless loop to provide an enhanced sawing and cutting action as noted above.

In Fig. 3B another cutting device 15 is shown again having vertical structural members 16 connected by an overhead horizontal member 26. A blade or other thin device extends between the vertical structural members 16. A vacuum port 29 is shown to remove molten solder as the blade is moved through the solder interconnect during cutting of the interconnect. As with the cutting device of Fig. 3A using a wire 17, the blade will move forward against the rows of solder balls 13 and cut the solder balls due to the thickness of the blade and the elevated temperature of the blade. The blade 21 may be provided with a reciprocating back and forth sawing motion to enhance the cutting action of the blade as shown in Fig. 4B. The blade can also be a continuous blade, i.e., band, as in a band saw. The thickness of the blade may vary widely and is less than the height  $h$  of the solder ball. A thickness of about 0.002 to 0.010 inch or more can be used. The width of the blade will generally be less than 50 mm although larger blades can be used depending on the solder interconnect to be cut.

In Fig. 3C the blade 30 is shown angled having angled edges 30a and 30b. This configuration enhances the plowing action of the blade 30 as it moves through the solder balls and pushes the removed solder along the edges to the vacuum ports 29.

In Fig. 3D another embodiment of the invention is shown with the cutting device 15 having vertical structural members 16 and a connecting overhead horizontal member 26. Extending between the vertical members 16 is a thin water jet 22 which water jet has a water inlet 23 and a water outlet 24. As with the other embodiments, the cutting device 15 will be forced to move forward across the surface of the printed circuit board against rows of the solder balls and the water from the outlet jets cut and sever the rows of solder balls 13. Other fluids may be used. A typical jet fluid pressure is about 20,000 to 60,000 psi. The thickness of the water jet is generally

about 0.002 to 0.040 inch. Commercial water jets made by Precision Cutting Systems, Jet Edge or JIT precision water jet cutting devices or any other suitable water jet can be used..

- 5 While the present invention has been particularly described, in conjunction with a specific preferred embodiment, it is evident that many alternatives, modifications and variations will be apparent to those skilled in the art in light of the foregoing description. It is therefore contemplated that the appended claims will embrace any such alternatives, modifications and variations as falling within the true
- 10 scope and spirit of the present invention.

Thus, having described the invention, what is claimed is: